

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/821,370
Filed: April 9, 2004
Inventor(s):
 Landin et al.
Examiner: Eland, Shawn
Group/Art Unit: 2188
Atty. Dkt. No: 5181-99901
Conf. No. 1207
Title: MULTI-NODE
 COMPUTER SYSTEM
 EMPLOYING MULTIPLE
 MEMORY RESPONSE
 STATES

**AMENDMENT; RESPONSE TO OFFICE ACTION OF
November 16, 2007**

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Office Action of November 16, 2007, to further highlight why the application is in condition for allowance.